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APPLICANTS

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None, TL

** FOREIGN APPLICATIONS *****

JAPAN 2002-353731 12/05/2002

yes, TL

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 2
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TITLE

Semiconductor memory device and method for correcting memory cell data

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